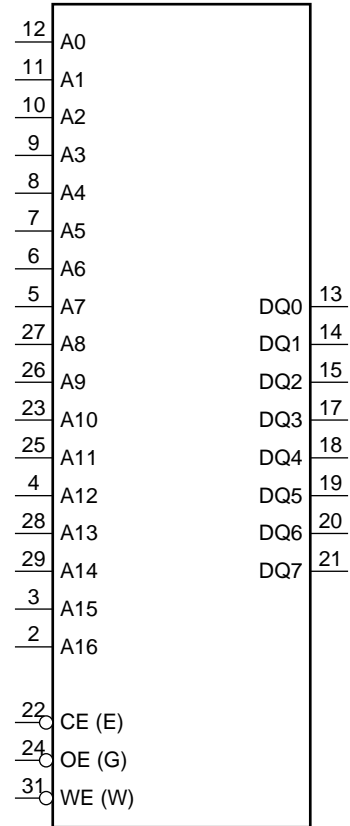
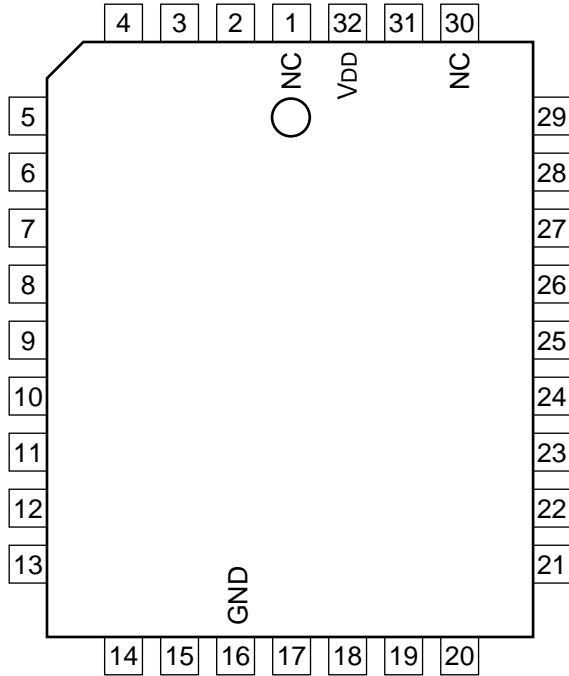


C-MOS 1 M (131,072 × 8)-BIT FLASH EEPROM

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	NC	17	I/O	DQ3
2	I	A16	18	I/O	DQ4
3	I	A15	19	I/O	DQ5
4	I	A12	20	I/O	DQ6
5	I	A7	21	I/O	DQ7
6	I	A6	22	I	$\overline{CE}$ ( $\overline{E}$ )
7	I	A5	23	I	A10
8	I	A4	24	I	$\overline{OE}$ ( $\overline{G}$ )
9	I	A3	25	I	A11
10	I	A2	26	I	A9
11	I	A1	27	I	A8
12	I	A0	28	I	A13
13	I/O	DQ0	29	I	A14
14	I/O	DQ1	30	—	NC
15	I/O	DQ2	31	I	$\overline{WE}$ ( $\overline{W}$ )
16	—	GND	32	—	VDD

**INPUT**

A0 - A16 ; ADDRESS (0 - 16)  
 CE ; CHIP ENABLE  
 OE ; OUTPUT ENABLE  
 WE ; WRITE ENABLE

**OUTPUT**

DQ0 - DQ7 ; DATA (0 - 7)

OPERATION	$\overline{CE}$	$\overline{OE}$	$\overline{WE}$	A0	A1	A9	I/O
AUTO-SELECT MANUFACTURER CODE (1)	0	0	1	0	0	VID	CODE
AUTO-SELECT DEVICE CODE (1)	0	0	1	1	0	VID	CODE
READ	0	0	1	A0	A1	A9	DOUT
STANDBY	1	X	X	X	X	X	HI-Z
OUTPUT DISABLE	0	1	1	X	X	X	HI-Z
WRITE	0	1	0	A0	A1	A9	DIN (2)
ENABLE SECTOR PROTECT	0	VID	0	X	X	VID	X
VERIFY SECTOR PROTECT (3)	0	0	1	0	1	VID	CODE

0 ; LOW LEVEL  
 1 ; HIGH LEVEL  
 X ; DONT CARE  
 HI-Z ; HIGH IMPEDANCE

